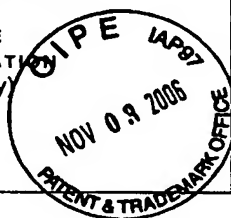


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/CN/		Behrooz, Parhami: "High-Radix Multipliers" Computer Arithmetic: Algorithms And Hardware Design, 2000, page 159, paragraph 10.2-161, figure 10.6
/CN/		Tenca, A. F. et al: "High-radix Design of A Scalable Modular Multiplier" cryptographic Hardware and Embedded Systems, 3 rd International Workshop, May 14, 2001 Vol. 2162, pages 185-201
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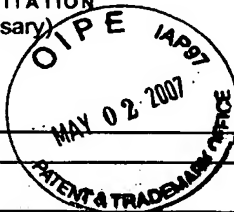
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